



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Appln Of: KIMURA  
Serial No.: 10/091,776  
Filed: March 5, 2002  
For: CMOS REFERENCE VOLTAGE CIRCUIT  
Group: 2816  
Examiner: Englund, Terry Lee DOCKET: NEC NEG-244

MAIL STOP ISSUE FEE  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

**SUBMISSION OF FORMAL DRAWINGS**

Dear Sir:

In response to the Notice of Allowability mailed January 13, 2005, submitted herewith are ten (10) sheets of formal drawings with respect to the above-identified patent application.

In the event there are any fee deficiencies or additional fees are payable, please charge them (or credit any overpayment) to our Deposit Account No. 08-1391.

Respectfully submitted,

Norman P. Soloway  
Attorney for Applicant  
Reg. No. 24,315

**CERTIFICATE OF MAILING**

I hereby certify that this correspondence is being deposited with the United States Postal Service as First Class Mail in an envelope addressed to: MAIL STOP ISSUE FEE, Commissioner of Patents and Trademarks, P.O. Box 1450, Alexandria, VA 22313-1450 on

April 6, 2005 at Tucson, Arizona.

By:

HAYES SOLOWAY P.C.  
130 W. CUSHING STREET  
TUCSON, AZ 85701  
TEL. 520.882.7623  
FAX. 520.882.7643

175 CANAL STREET  
MANCHESTER, NH 03101  
TEL. 603.668.1400  
FAX. 603.668.8567